

# SKW Associates, Inc.

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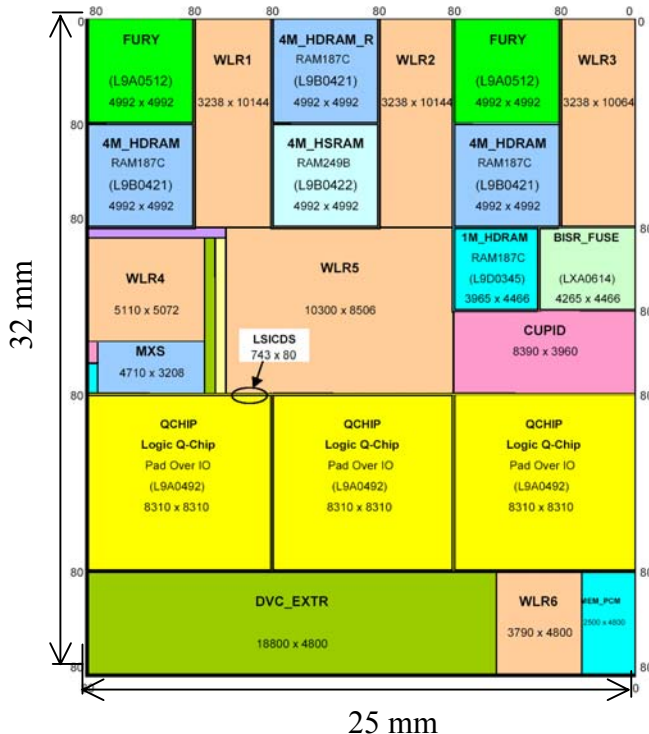
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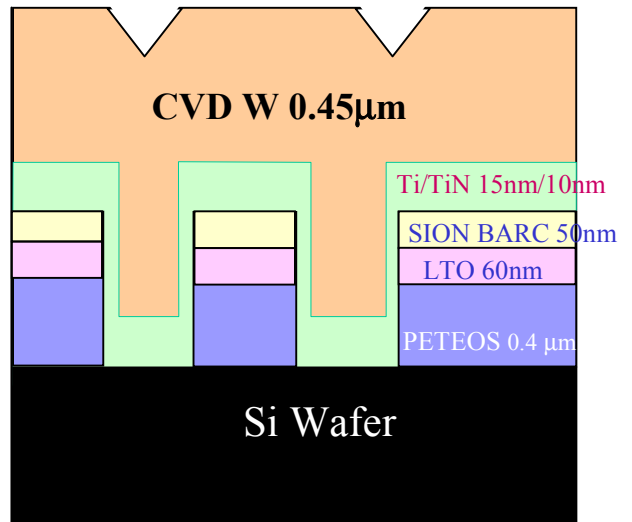
<http://www.testwafer.com>

# SKW 5-J085 Wafer Specifications

Rev. DATE: Jul 30, 2005

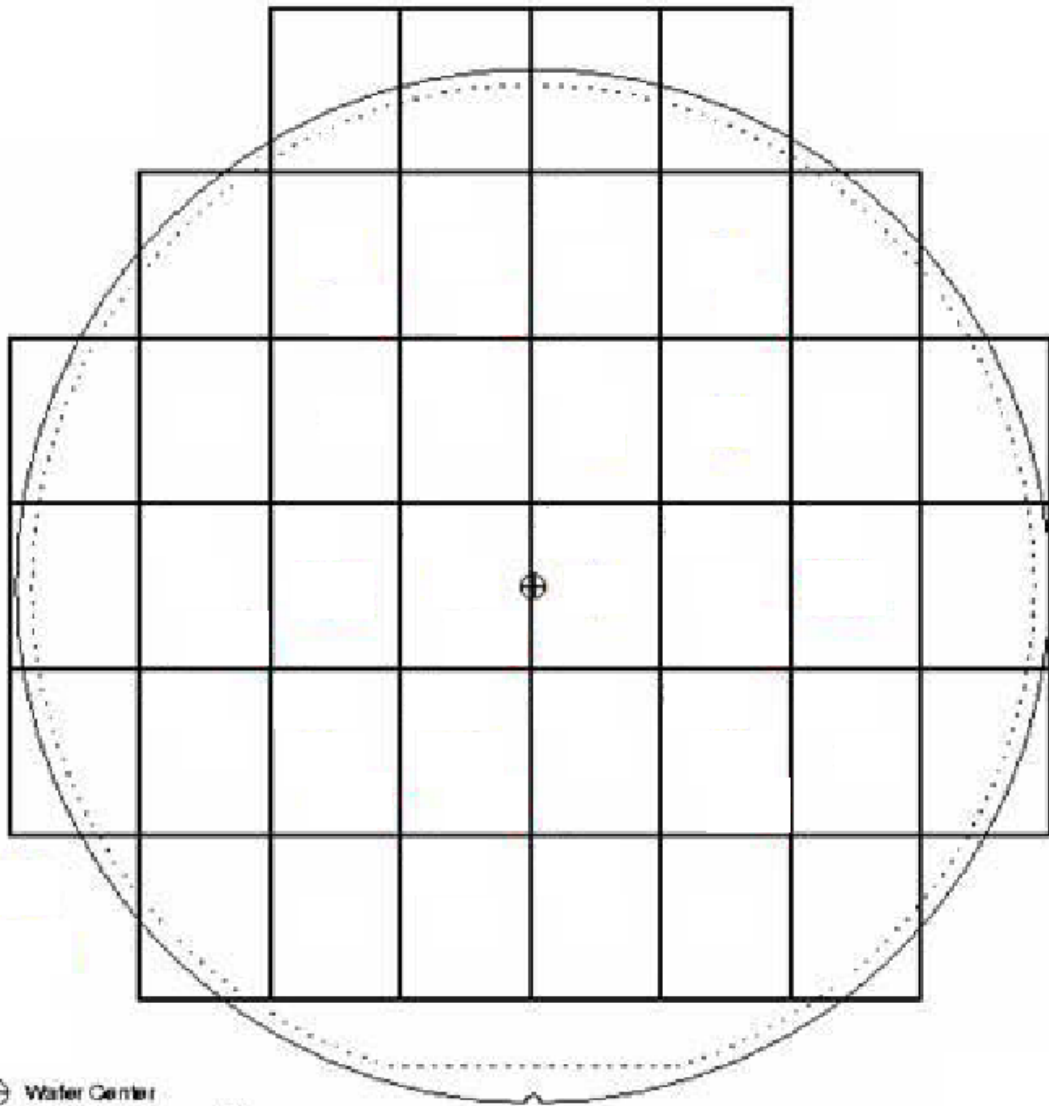


SKW 5-J085 Mask Floor Plan



Cross Sectional View

PARAMETER	NOMINAL	TOLERANCE
Patterning		
Die Size: X	25 mm	+/- 10 μm
Die Size: Y	32 mm	+/- 10 μm



## SKW 5-J085: Die Distribution Over Wafer

PARAMETER	NOMINAL	TOLERANCE
<b>Minimum Plug Hole Diameter</b>		
Lot-to-Lot	160 nm	+/- 10 nm
Within-Lot (Wafer-to-Wafer)		+/- 10 nm
Within-Wafer		+/- 10 nm
Within-Die		+/- 10 nm
<b>PETEOS thickness</b>		
Lot-to-Lot	4000 Å	+/- 5 %
Within-Lot (Wafer-to-Wafer)		+/- 5 %
Within-Wafer		+/- 3 %
Within-Die		+/- 3 %
<b>SION BARC thickness</b>		
Lot-to-Lot	500 Å	+/- 5 %
Within-Lot (Wafer-to-Wafer)		+/- 5 %
Within-Wafer		+/- 3 %
Within-Die		+/- 3 %
<b>LTO thickness</b>		
Lot-to-Lot	600 Å	+/- 5 %
Within-Lot (Wafer-to-Wafer)		+/- 5 %
Within-Wafer		+/- 3 %
Within-Die		+/- 3 %
<b>Ti thickness</b>		
Lot-to-Lot	150 Å	+/- 5 %
Within-Lot (Wafer-to-Wafer)		+/- 5 %
Within-Wafer		+/- 3 %
Within-Die		+/- 3 %

PVD TiN film thickness	100 Å	
Lot-to-Lot		+/- 5 %
Within-Lot (Wafer-to-Wafer)		+/- 5 %
Within-Wafer		+/- 5 %
Within-Die		+/- 5 %
CVD W film thickness	4500 Å	
Lot-to-Lot		+/- 10 %
Within-Lot (Wafer-to-Wafer)		+/- 10 %
Within-Wafer		+/- 5 %
Within-Die		+/- 5 %